

FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>	Docket Number (Optional) 81790.0311	Application Number <i>10/768, 238</i> Not assigned
	Applicant YAEGASHI et al.	
	Filing Date January 29, 2004	Group Art Unit <i>2815</i> Not assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>ONE</i>	5,989,957	11-1999	Ngo et al.	438	257	
	5,841,174	11-1998	Arai	257	392	
	5,852,311	12-1998	Kwon et al.	257	315	
	5,913,120	06-1999	Cappelletti	438	257	
	5,600,164	02-1997	Ajika et al.	257	321	
<i>ONE</i>	5,824,583	10-1998	Asano et al.	438	258	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
<i>ONE</i>	62-045165	02-1987	Japan				
	08-23041	01/23/96	Japan				
	05-190811	07/30/93	Japan				
	05-183134	07-1993	Japan	438	396		
	02-246376	10-1990	Japan	257	320		
	62-23149	7/24/85	Japan	<i>HOIL</i>	27/10		
	62-76668	9/30/85	Japan	<i>HOIL</i>	27/10		
<i>ONE</i>	7-297304	3/24/95	Japan	<i>HOIL</i>	21/8247		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>ONE</i>	HANNON, et al., "0.25 μ m Merged Bulk DRAM and SOI Logic Using Patterned SOI", 2000 Symposium on VLSI Technology Digest of Technical Papers pages, 66 - 67 *
	SCHEUERLEIN, et al., "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in each Cell", ISSCC 2000/Session 7/TD: Emerging Memory & Device Technologies/ Paper TA 7.2, pages 128 - 129 *
	YAMADA, et al., "An Embedded DRAM Technology on SOI/Bulk Hybrid Substrate Formed with SEG Process for High-End SOC Application", 2002 Symposium On VLSI Technology Digest of Technical Papers, pages 112 - 113 *
	HANNON, et al., "0.25 μ m Merged Bulk DRAM and SOI Logic Using Patterned SOI", 2000 Symposium on VLSI Technology Digest of Technical Papers pages, 66 - 67 *
<i>ONE</i>	Wolf, et al. "Silicon Processing for the VLSI Era," 1986, Lattice Press, Vol. 1, pages 384-388 *

** no month cited.*

EXAMINER <i>ONE</i>	DATE CONSIDERED <i>4/29/05</i>
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.